

# HS/M 16 Encoder

## Features:

- Integrated, compact dual encoder in the standard HeiMotion modular system
- Singleturn with SSI and sin/cos
- Multiturn with BiSS-C<sup>3)</sup> and sin/cos
- Speeds up to 12,000 min<sup>-1</sup>
- Temperature evaluation via BiSS-C possible
- Electronic nameplate possible on request



## Specifications

### HS 16 (singleturn)

### HM 16 (multiturn)

	HS 16 (singleturn)	HM 16 (multiturn)
Supply voltage	5.0 V <sub>DC</sub> +10/-5%	5.0 V <sub>DC</sub> +10/-5%
Typical output current (without load)	120 mA	120 mA
Power consumption	0.6 W	0.6 W
Max. resolution singleturn	16 bit <sup>2)</sup>	16 bit <sup>2)</sup>
Max. number of absolute revolutions detected	-	12 bit (mechanical)
Data interface	SSI gray (RS422) + SinCos 1V <sub>pp</sub>	BiSS-C (RS422) <sup>3)</sup> + SinCos 1V <sub>pp</sub>
Sin/cos tracks	differential	differential
Number of sin/cos periods per revolution	256 (8 bit)	256 (8 bit)
Max. angular acceleration	100,000 rad/sec <sup>2</sup>	100,000 rad/sec <sup>2</sup>
Resistance to shocks (DIN EN 60068-2-27)	3,000 m/s <sup>2</sup> (6 ms)	3,000 m/s <sup>2</sup> (6 ms)
Resistance to vibration (DIN EN 60068-2-6)	300 m/s <sup>2</sup>	300 m/s <sup>2</sup>
Operating temperature	-40°C / +120°C	-40°C / +120°C
Storage temperature	-30°C / +80°C	-30°C / +80°C
Order code	XXS1SXXXX	XXB1MXXXX

## Safety parameters

- Analog output for monitoring functional safety
- SIL2, PL d safety certified<sup>1)</sup>

## Specifications

### HS 16S (singleturn)

### HM 16S (multiturn)

	HS 16S (singleturn)	HM 16S (multiturn)
Security integrity level <sup>1)</sup> (EN IEC 61508, 62061, 61800-5-3)	SIL2	SIL2
Performance level <sup>1)</sup> (EN ISO 13849-1)	PL d	PL d
Functional safety architecture <sup>1)</sup>	Canal 1: sin/cos      Canal 2: SSI	Canal 1: sin/cos      Canal 2: SSI
Electrical interface <sup>1)</sup>	1V <sub>pp</sub> 2.5V <sub>DC</sub> (differential signal - sin+cos) + SSI (RS422)	1V <sub>pp</sub> 2.5V <sub>DC</sub> (differential signal - sin+cos) + BiSS-C (RS422)
Resolution for safety function <sup>1)</sup>	512 sin+cos periods (9 bit)	512 sin+cos periods (9 bit)
Order code	XXSASXXXX	XXBAMXXXX

<sup>1)</sup> Still in implementation

<sup>2)</sup> 20 bit on request

<sup>3)</sup> SSI gray on request

Technical data subject to change! Last changes: 10/2024

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